

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	1427	integrated adj circuit adj card	USPA T; US-P GPUB	2004/02/2 3 17:31	
2	BRS	L2	7783	ic adj card	USPA T; US-P GPUB	2004/02/2 3 17:31	
3	BRS	L3	8557	1 or 2	USPA T; US-P GPUB	2004/02/2 3 17:32	
4	BRS	L4	24438	memory near5 stor\$3 near5 (charge or power or voltage)	USPA T; US-P GPUB	2004/02/2 3 17:32	
5	BRS	L5	444	3 and 4	USPA T; US-P GPUB	2004/02/2 3 17:33	
6	BRS	L6	34997	interrupt\$3 near5 (power or voltage)	USPA T; US-P GPUB	2004/02/2 3 17:33	
7	BRS	L7	16143	interrupt\$3 near5 (read\$3 or writ\$4)	USPA T; US-P GPUB	2004/02/2 3 17:36	
8	BRS	L8	49	5 and 6	USPA T; US-P GPUB	2004/02/2 3 17:36	
9	BRS	L9	13	7 and 8	USPA T; US-P GPUB	2004/02/2 3 18:00	
10	BRS	L10	36	8 not 9	USPA T; US-P GPUB	2004/02/2 3 18:34	
11	BRS	L11	30	5 and 7	USPA T; US-P GPUB	2004/02/2 3 18:34	
12	BRS	L12	17	11 not 9	USPA T; US-P GPUB	2004/02/2 3 18:37	
13	BRS	L13	1051	feram	USPA T; US-P GPUB	2004/02/2 3 18:37	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
14	BRS	L14	8	5 and 13	USPA T; US-P GPUB	2004/02/23 18:42	
15	BRS	L15	1579	713/300.ccls. 713/330.ccls. 713/340.ccls.	USPA T; US-P GPUB	2004/02/23 18:43	
16	BRS	L16	10	5 and 15	USPA T; US-P GPUB	2004/02/23 18:43	